



App No.: 10/602,721

Docket No.: M4065.0761/P761

Inventor: Chandra Mouli, et al.

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE  
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE  
TRANSFER AND REDUCED LAG AND METHOD OF  
FORMATION

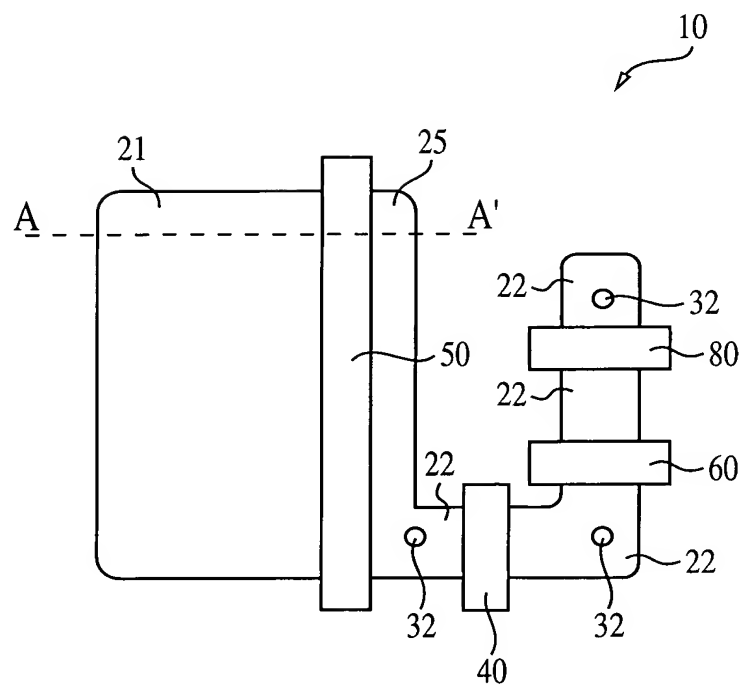


FIG. 1  
(PRIOR ART)



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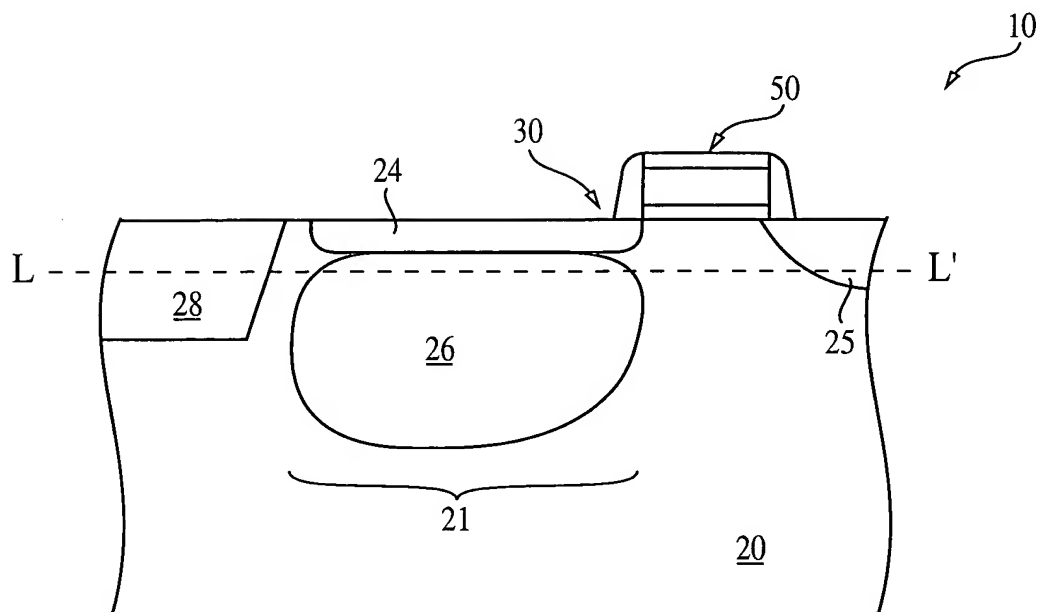


FIG. 2  
(PRIOR ART)



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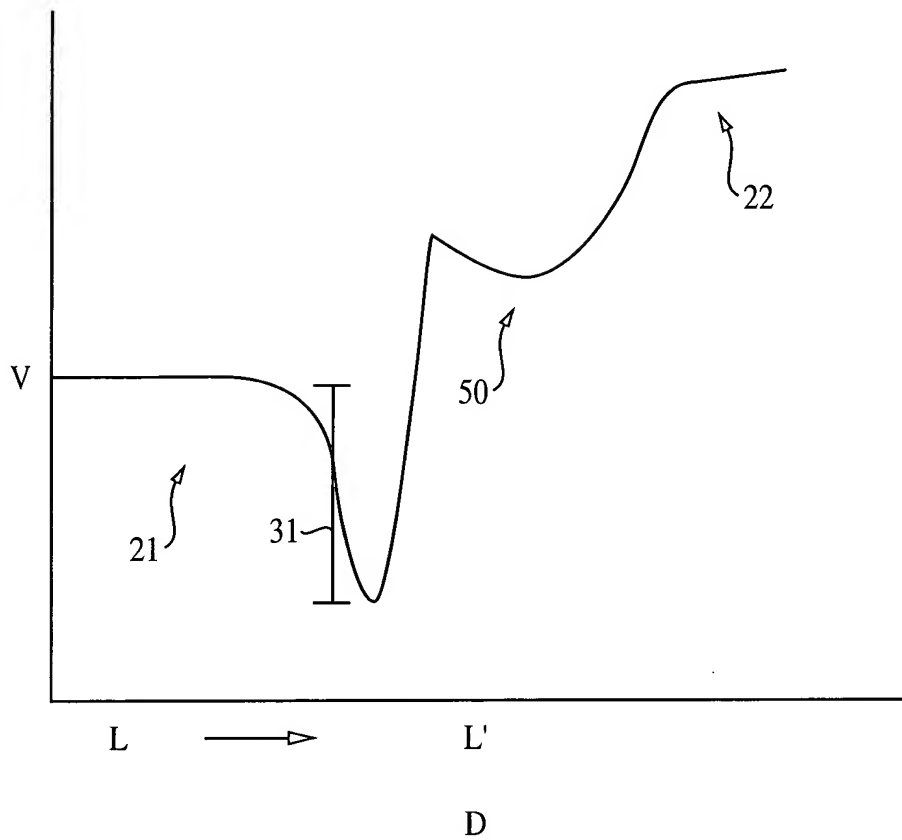


FIG. 3



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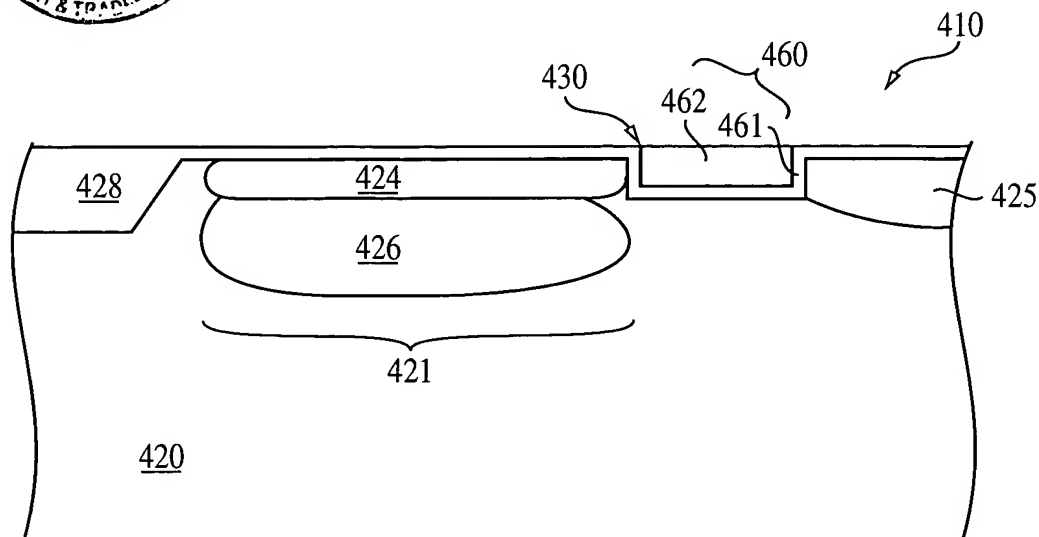


FIG. 4

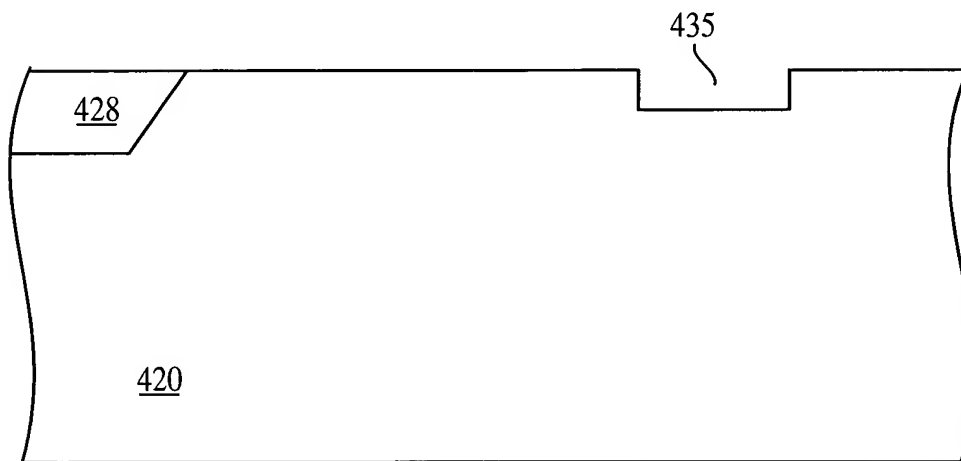


FIG. 5A

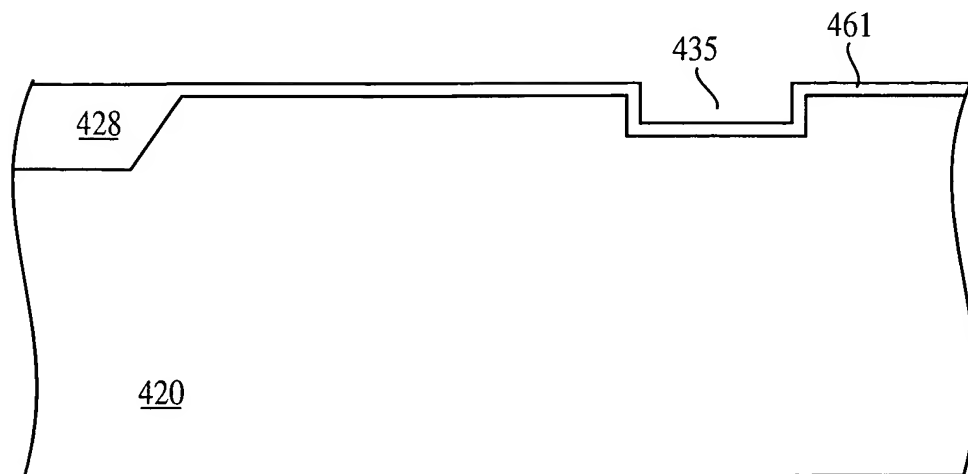
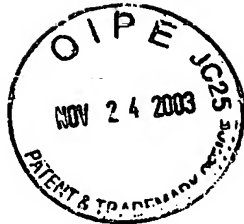


FIG. 5B

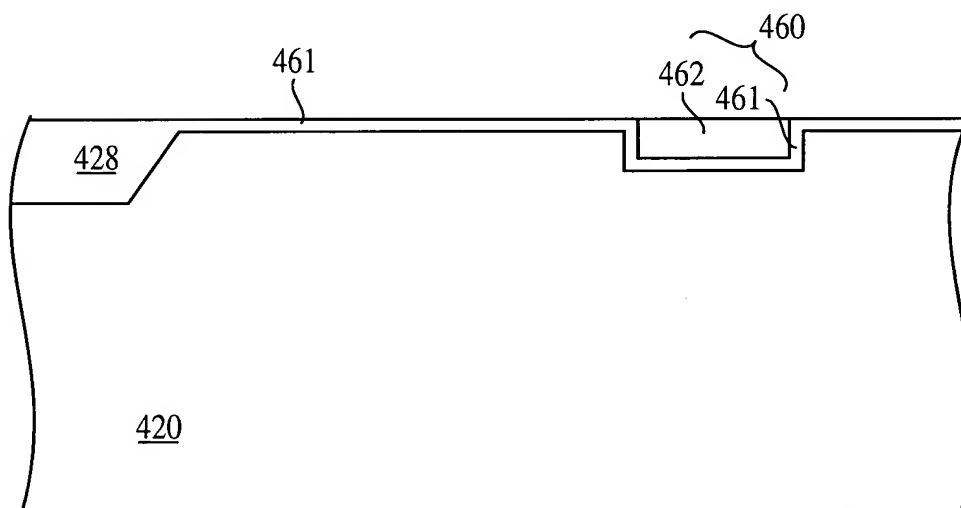


FIG. 5C



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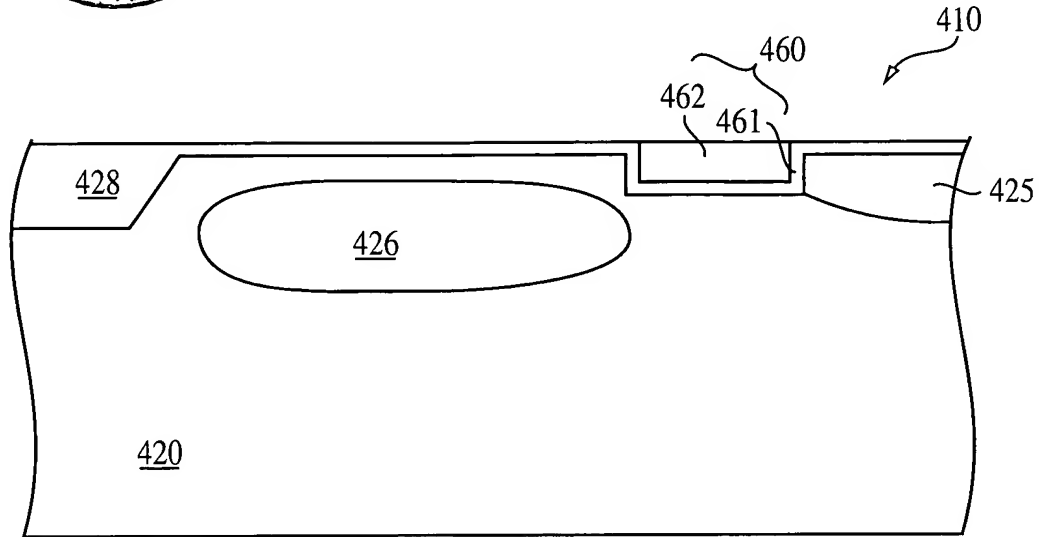


FIG. 5D

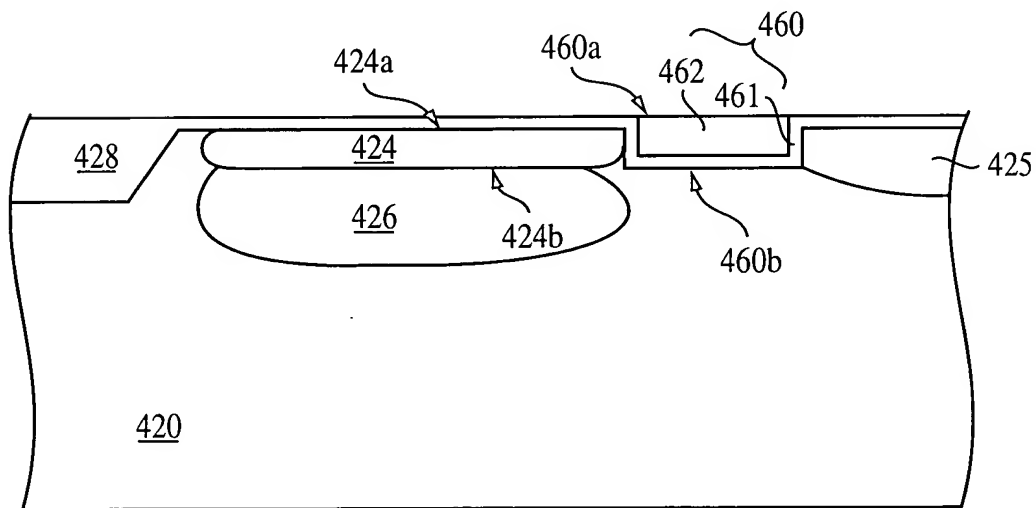


FIG. 5E

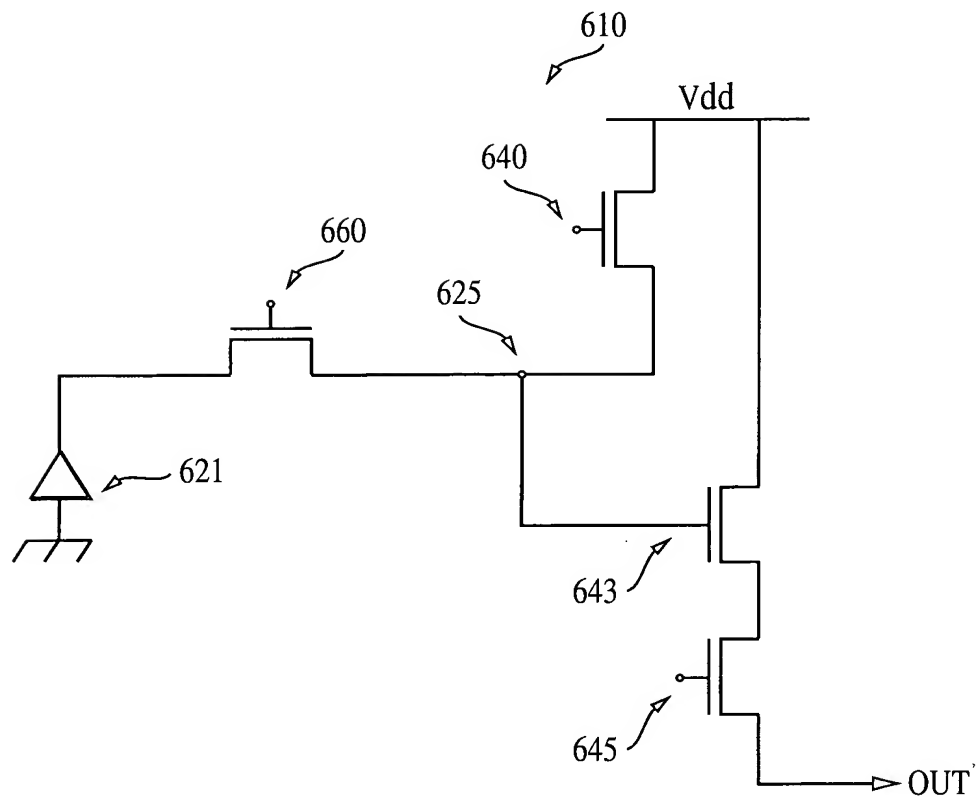


FIG. 6



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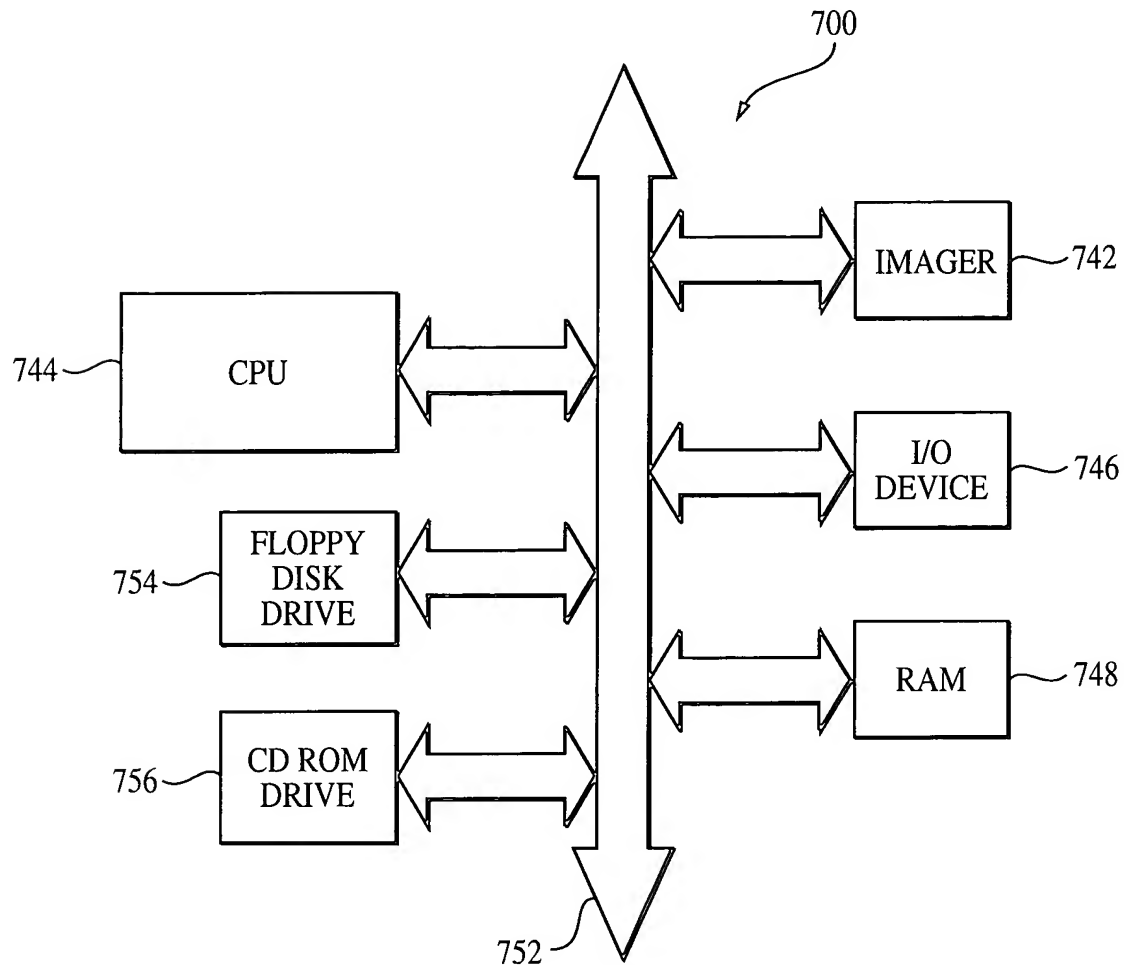


FIG. 7